



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kou Nagata, et al.

Art Unit: 2827

Application No.: 10/823,572

Conf. No. 4816

Filed: April 14, 2004

Examiner: T.Q. Phan

For: SEMICONDUCTOR MEMORY APPARATUS  
AND SELF-REPAIR METHOD

**AFTER FINAL RESPONSE AND REQUEST FOR RECONSIDERATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enter  
TP  
12/13/05

Dear Sir:

In full and timely response to the Final Office Action dated June 21, 2005, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 12 of this paper.